

IN THE TITLE:

Amend the title to read as follows:

PLL Circuit Which Compensates For Stoppage Of PLL Operations

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claim 1 (previously presented): A PLL circuit in which a phase comparator, a loop filter, a voltage control oscillator and a frequency divider are successively loop-connected, said PLL circuit comprising:

5, operation stoppage detecting means for detecting that PLL operation has stopped, said detection being effected on the basis of an output signal from said voltage control oscillator or said frequency divider; and

control means for, when said operation stoppage detecting means detects stoppage of operation, controlling the voltage control oscillator such that an oscillation frequency of the voltage control oscillator is low.

Claim 2 (original): A PLL circuit according to claim 1, wherein said operation stoppage detecting means is a means for detecting presence/absence of an output signal of the frequency divider.

Claim 3 (canceled)

Claim 4 (currently amended): A PLL circuit according to claim 1, wherein said operation stoppage detecting means is a means for detecting whether or not an oscillation frequency of the voltage control oscillator is a value higher than a predetermined value.

Claim 5 (currently amended): A PLL circuit according to any one of claims 1 ~~through 4~~, 2 or 4, wherein said control means is a means for switching an output of the phase comparator to a value at which an oscillation frequency of the voltage control oscillator decreases.

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Claim 6 (currently amended): A PLL circuit according to any one of claims 1 ~~through 4~~, 2 or 4, wherein said control means is a means for switching a comparison signal inputted to the phase comparator such that an oscillation frequency of the voltage control oscillator decreases.
